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John Snyder John Larson

Fabrication method for a device for regulating flow of electric current with high dielectric constant gate insulating layer and source/drain forming schottky contact or schottky-like region with substrate

ISSUING CLASSIFICATION ORIGINAL CROSS REFERENCE(S) CLASS **SUBCLASS CLASS** SUBCLASS (ONE SUBCLASS PER BLOCK) INTERNATIONAL CLASSIFICATION Continued on Issue Slip Inside File Jacket

TERMINAL	DRAWINGS			CLAIMS ALLOWED		
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent subsequent to (date)				NOTICE OF ALLOWANCE MAILED		
has been disclaimed.	(Assistant Examiner)		(Date)			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No				ISSI	JE FEE	
				Amount Due	Date Paid	
	(Primary Examiner)		(Date)			
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orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RON	

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